

PATENT  
Serial No. 09/514,388  
Amendment in Reply to Office Action of October 5, 2005

IN THE SPECIFICATION

Please amend the specification as follows:

Replace the paragraph spanning pages 4-5, between page 4, line 31, and page 5, line 6 of the specification with the following:

In order to display teletext signals, various timing signals have to be derived from the video signal applied to the video processor 42. These timing signals are recovered by a timing chain stage or TIC stage 48. The TIC stage 48 has a parallel data bus 50 coupled to a teletext ROM (or TROM) 52. An output of the memory 46 is coupled by a parallel data bus 54 to another input of the TROM 52. The TROM 42-52 has an output 56 coupled to the signal processor 34. The TIC stage 48 has another output 58 coupled to a timebase stage 60 which supplies timing signals to the cathode ray tube 38.